



**IN THE SPECIFICATION:**

Please replace paragraph [0001] with the following amended paragraph:

[0001] The present invention is related to the commonly owned, co-pending U.S. patent application entitled "Back-Bias Voltage Generator With Temperature Control," filed ~~herewith~~ on November 19, 2003, application serial number 10/716,762 (Attorney Docket No. 2003P52878US).

Please replace paragraph [0029] with the following amended paragraph:

[0029] For one embodiment, each one of the temperature control signals TD[0:N] may correspond to a distinct temperature range, with only one signal driven high at any given time. In other words, a different voltage level for  $V_{PP}$  to be selected by driving a different one of the signals high when the device temperature is in the corresponding range. TABLE I below illustrates a set of exemplary temperature ranges that may

**TABLE I: Exemplary Temperature Ranges**

TD	Temperature Range
0	$T > 75C$
1	$75C > T > 50$
2	$50C > T > 25$
3	$T < 25$

be represented by four temperature control signals TD[0:3] (i.e., TD[0:N], with N=3). Of course, the temperature ranges shown in Table I are illustrative only, and any number of temperature ranges may be used, depending on the application.

Please replace paragraph [0030] with the following amended paragraph:

[0030] FIG. 3 illustrates one embodiment of the  $V_{PP}$  voltage generator 230, that generates different voltage levels, based on the temperature control signals TD[0:3] (i.e., TD[0:N], with N=3). As illustrated, the temperature control signals TD[0:3] may be generated from two bits of temperature information T[0:1] stored in a mode register 320 input to a decoder 322 that enables (e.g., pulls high) one of the signals TD[0:3], in response to the values of T[0:1]. For some embodiments, the temperature information T[0:1] may indicate device temperature as indicated by an external temperature sensor.

Please replace paragraph [0042] with the following amended paragraph:

[0042] The control logic 536 may include any suitable circuitry configured to control the switching of the various nodes (T0-T100) to the comparators 532 and 534, in an effort to generate the temperature control signals TD[0:3], for example, to indicate device temperature is in a corresponding range. As illustrated, the control logic 536 may control switch pairs S0-S3 (i.e., switch pairs S0, S1, S2, and S3 are formed by discrete switches S0H and S0L, S1H and S1L, S2H and S2L, and S3H and S3L, respectively), each with a different one of the control signals TD[0:3], to supply node voltages corresponding to low and high temperatures to the positive input of each of the comparators 532 and 534, respectively. The negative input of each of the comparators 532 and 534 may be coupled to the diode 524. The output of each comparator will indicate whether the ambient temperature is below or above the temperature corresponding to the node voltage. Thus, by applying different reference voltages to the comparators 532 and 534, the control logic 536 may determine if the ambient temperature is within a certain range by examining the comparator outputs.